

A Novel Architecture towards Exascale Computing

Thursday, March 9, 2017 5:00 PM (30 minutes)

As data volumes grow rapidly in the science domain, the ability to process this data efficiently is becoming increasingly of interest. While in many applications the processing of very large volumes can be accomplished efficiently with map/reduce algorithms (e.g., using frameworks such as Hadoop), this does not cover a large class of problems which are best run in an HPC environment. This class of problem requires a new paradigm commonly referred to as Big Data and Extreme Computing (BDEC).

This talk explains why current HPC systems are not fully suited to BDEC class problems, and how some upcoming technologies such as advanced non-volatile memories could help to resolve some of these issues. We introduce a novel architecture that aims to address this problem, currently being developed within the SAGE project (sagestorage.eu) as part of the Horizon2020 program. We explain how this architecture has been co-designed between leading industrial partners and research organisations covering a wide spectrum of scientific disciplines, and outline new programming models that are being developed to make use of this new technology, including tools to optimise its use. Finally, we present details of testing of this new architecture and explain where such a system can overcome the limitations of traditional HPC systems.

Primary author: Mr DE WITT, Shaun (Culham Centre for Fusion Energy)

Co-authors: Dr BOND, David (Diamond Light Source); Dr BRABAZON, Keeran (Allinea Software Ltd.); Dr PERKS, Oliver (Allinea Software Ltd.); Dr NARASIMHAMURTHY, Sai (Seagate Technology); Mr EL SAYED MOHAMED, Salem (Jülich Supercomputing Centre); Mr GÓMEZ, Sergio Rivas (KTH Royal Institute of Technology); Prof. MARKIDIS, Stefano (KTH Royal Institute of Technology)

Presenter: Mr DE WITT, Shaun (Culham Centre for Fusion Energy)

Session Classification: Supercomputing, High Throughput, Accelerator Technologies and Integration

Track Classification: Supercomputing, High Throughput, Accelerator Technologies and Integrations